

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
5 August 2004 (05.08.2004)

PCT

(10) International Publication Number  
**WO 2004/066477 A3**

(51) International Patent Classification<sup>7</sup>: **H01L 51/40**,  
51/20

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(21) International Application Number:  
PCT/GB2004/000161

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(22) International Filing Date: 19 January 2004 (19.01.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
0301089.9 17 January 2003 (17.01.2003) GB

(81) Designated States (*unless otherwise indicated, for every  
kind of national protection available*): AE, AG, AL, AM,  
AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,  
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,  
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,  
KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD,  
MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG,  
PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM,  
TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM,  
ZW.

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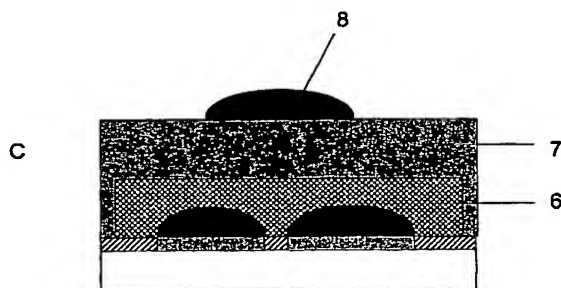
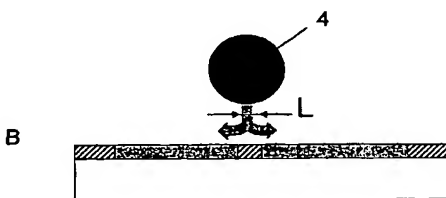
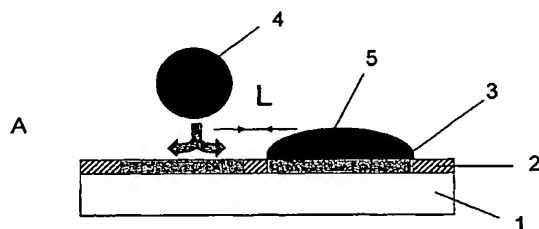
(84) Designated States (*unless otherwise indicated, for every  
kind of regional protection available*): ARIPO (BW, GH,  
GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),

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[Continued on next page]

(54) Title: SEMICONDUCTING ISLAND AS ACTIVE LAYER IN FET



(57) Abstract: A method for forming an electronic device including at least one electrically conductive and one semiconductive material deposited from solution, the method comprising: forming on the substrate a confinement structure consisting of a least a first zone and a second zone, depositing the electrically conductive material on the substrate, wherein the electrically conductive material is absent from both the first and second zone, and subsequently depositing the electrically semiconductive material from solution, wherein the semiconductive material is absent from the first zone, but not from the second zone.



Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

**Published:**

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

**(88) Date of publication of the international search report:**  
27 January 2005

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

## INTERNATIONAL SEARCH REPORT

Intern Application No  
PCT/GB2004/000161A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 H01L51/40 H01L51/20

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, COMPENDEX

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

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Y	the whole document	
X	WO 01/46987 A (PLASTIC LOGIC LTD ; SIRRINGHAUS HENNING (GB); KAWASE TAKEO (GB); FRIEN) 28 June 2001 (2001-06-28)	1,2,5-7, 10, 12-19, 22, 33-35, 37,38, 48,50,51 45-47
Y	page 6, line 17 - page 7, line 3 page 42, line 17 - line 21; figure 21a ----- -/--	

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

24 November 2004

Date of mailing of the international search report

02/12/2004

Name and mailing address of the ISA

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# INTERNATIONAL SEARCH REPORT

Inter Application No  
PC1/GB2004/000161

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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A	CHABINYC MICHAEL L ET AL: "Organic polymeric thin-film transistors fabricated by selective dewetting" APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, vol. 81, no. 22, 25 November 2002 (2002-11-25), pages 4260-4262, XP012032662 ISSN: 0003-6951 page 4261, column 1, line 4 - line 21; figure 1	1,50
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PCT/JP2004/000161

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